**XAPP Abstract for AMP baremetal/baremetal**

**Title**: Running Baremetal on Both Zynq Processors

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**Issue Being Resolved:**

Xilinx does not provide a method to run separate OSes on each processor within the Zynq PS (Processor System). This xapp will include software, hardware, and documentation to demonstrate baremetal running on both processors.

**Customer base:**

Any customer, who wishes to exercise both processors within Zynq, will benefit from this xapp. A growing number of customers have been asking how to run baremetal on both processors.

Current customers using/referencing this information:

* Sirius
* Cassidian-Optronics
* General Electric
* Embedded Specialist FAEs have had access to the information and some have forwarded it to their customers

**Design Approach:**

There are customers currently using the design approach that will be captured by this xapp. The design will be exposed to the customers and the xapp will document weaknesses of this approach compared to alternative approaches. Tens of customers have been asking how to implement what this xapp will cover.

**Duplication of Material:**

There is no material that covers this xapp. An AR has been created in order to assist with one step that will be outlined in this xapp.

**Completeness/accuracy of the design concept:**

The xapp will be complete with a walk through to re-create the design and run it.